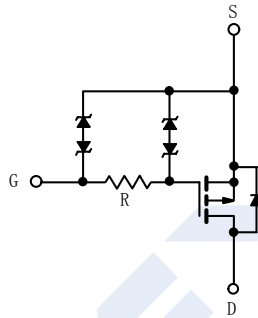
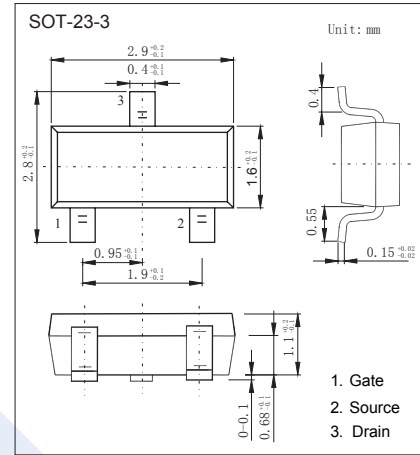


## P-Channel MOSFET

### SI2377EDS (KI2377EDS)

#### ■ Features

- $V_{DS} (V) = -20V$
- $I_D = -4.4 A$
- $R_{DS(ON)} < 61m\Omega$  ( $V_{GS} = -4.5V$ )
- $R_{DS(ON)} < 80m\Omega$  ( $V_{GS} = -2.5V$ )
- $R_{DS(ON)} < 110m\Omega$  ( $V_{GS} = -1.8V$ )
- $R_{DS(ON)} < 165m\Omega$  ( $V_{GS} = -1.5V$ )



#### ■ Absolute Maximum Ratings $T_a = 25^\circ C$

Parameter		Symbol	Rating	Unit
Drain-Source Voltage		$V_{DS}$	-20	V
Gate-Source Voltage		$V_{GS}$	$\pm 8$	
Continuous Drain Current ( $T_J = 150^\circ C$ )	$T_c = 25^\circ C$	$I_D$	-4.4	A
	$T_c = 70^\circ C$		-3.5	
	$T_a = 25^\circ C$		-3.7	
	$T_a = 70^\circ C$		-2.9	
Pulsed Drain Current		$I_{DM}$	-20	
Power Dissipation	$T_c = 25^\circ C$	$P_D$	1.8	W
	$T_c = 70^\circ C$		1.1	
	$T_a = 25^\circ C$		1.25	
	$T_a = 70^\circ C$		0.8	
Thermal Resistance.Junction- to-Ambient	$t \leq 5 s$	$R_{thJA}$	100	$^\circ C/W$
Thermal Resistance.Junction- to-Foot		$R_{thJF}$	70	
Junction Temperature		$T_J$	150	$^\circ C$
Storage Temperature Range		$T_{stg}$	-55 to 150	

## P-Channel MOSFET

### SI2377EDS (KI2377EDS)

#### ■ Electrical Characteristics Ta = 25°C

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit	
Drain-Source Breakdown Voltage	V <sub>DSS</sub>	I <sub>D</sub> =-250 μA, V <sub>GS</sub> =0V	-20			V	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-20V, V <sub>GS</sub> =0V			-1	μA	
		V <sub>DS</sub> =-20V, V <sub>GS</sub> =0V, T <sub>J</sub> =55°C			-10		
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =±8V			±6	μA	
		V <sub>DS</sub> =0V, V <sub>GS</sub> =±4.5V			±0.5		
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250 μA	-0.4		-1	V	
Static Drain-Source On-Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-3.2A			61	mΩ	
		V <sub>GS</sub> =-2.5V, I <sub>D</sub> =-2.8A			80		
		V <sub>GS</sub> =-1.8V, I <sub>D</sub> =-1.5A			110		
		V <sub>GS</sub> =-1.5V, I <sub>D</sub> =-0.5A			165		
On State Drain Current	I <sub>D(ON)</sub>	V <sub>GS</sub> =-4.5V, V <sub>DS</sub> ≤-5V	-15			A	
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> =-10V, I <sub>D</sub> =-3.2A		12		S	
Gate Resistance	R <sub>g</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, f=1MHz	0.4	2	4	KΩ	
Total Gate Charge	Q <sub>g</sub>	V <sub>GS</sub> =-8V, V <sub>DS</sub> =-10V, I <sub>D</sub> =-5.3A		14	21	nC	
					7.6		12
Gate Source Charge	Q <sub>gs</sub>	V <sub>GS</sub> =-4.5V, V <sub>DS</sub> =-10V, I <sub>D</sub> =-5.3A		0.8			
Gate Drain Charge	Q <sub>gd</sub>			3.1			
Turn-On DelayTime	t <sub>d(on)</sub>	V <sub>DD</sub> =-10V, R <sub>L</sub> =2.3Ω I <sub>D</sub> =-4.3A, V <sub>GEN</sub> =-4.5V, R <sub>g</sub> =1Ω		0.2	0.3	ns	
Turn-On Rise Time	t <sub>r</sub>			1	1.5		
Turn-Off DelayTime	t <sub>d(off)</sub>			4	6		
Turn-Off Fall Time	t <sub>f</sub>			2	3		
Turn-On DelayTime	t <sub>d(on)</sub>	V <sub>DD</sub> =-10V, R <sub>L</sub> =2.3Ω I <sub>D</sub> =-4.3A, V <sub>GEN</sub> =-8V, R <sub>g</sub> =1Ω		0.09	0.14	ns	
Turn-On Rise Time	t <sub>r</sub>			0.4	0.6		
Turn-Off DelayTime	t <sub>d(off)</sub>			5.2	7.8		
Turn-Off Fall Time	t <sub>f</sub>			2.3	3.5		
Body Diode Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> =-3A, di/dt=100A/μs, T <sub>J</sub> =25°C		30	60	nC	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>			20	40		
Reverse Recovery Fall Time	t <sub>a</sub>			13			ns
Reverse Recovery Rise Time	t <sub>b</sub>			17			
Maximum Body-Diode Continuous Current	I <sub>S</sub>	T <sub>c</sub> =25°C			-1.5	A	
Pulse Diode Forward Current (t = 100 μs)	I <sub>SM</sub>				-20		
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =-3A, V <sub>GS</sub> =0V			-1.2	V	

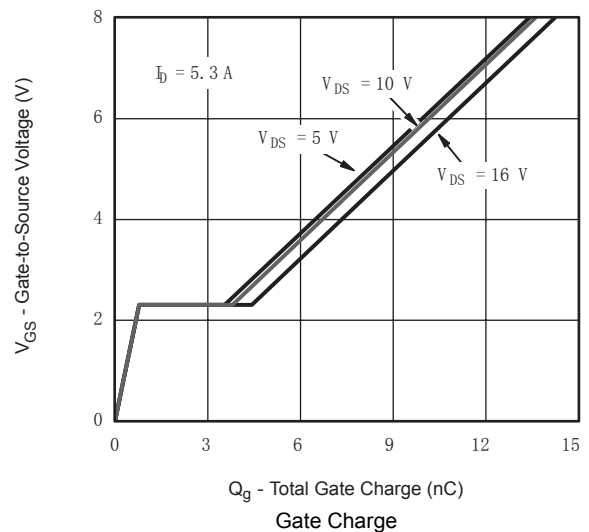
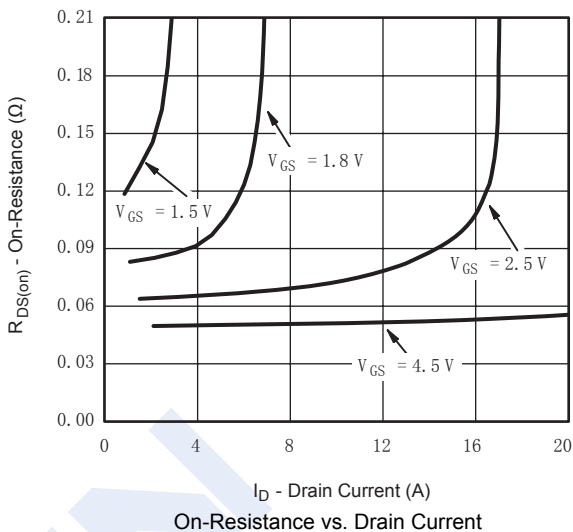
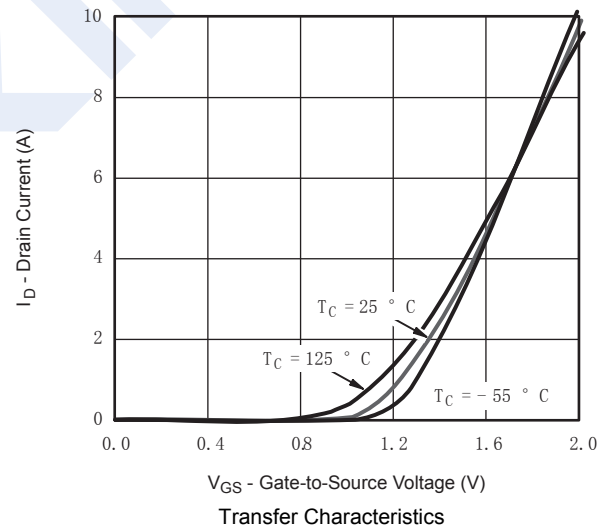
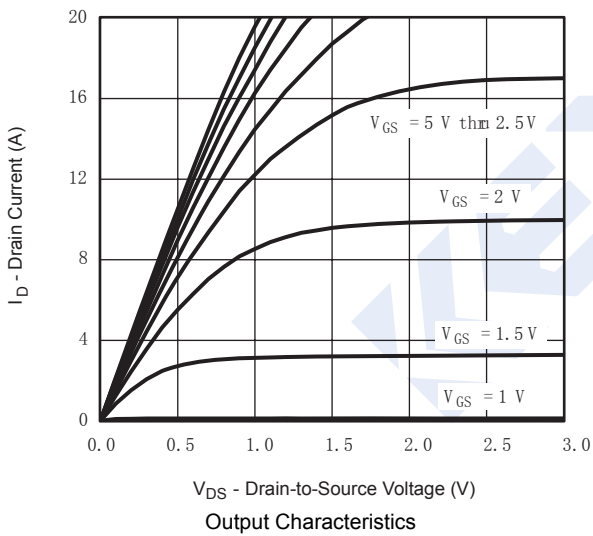
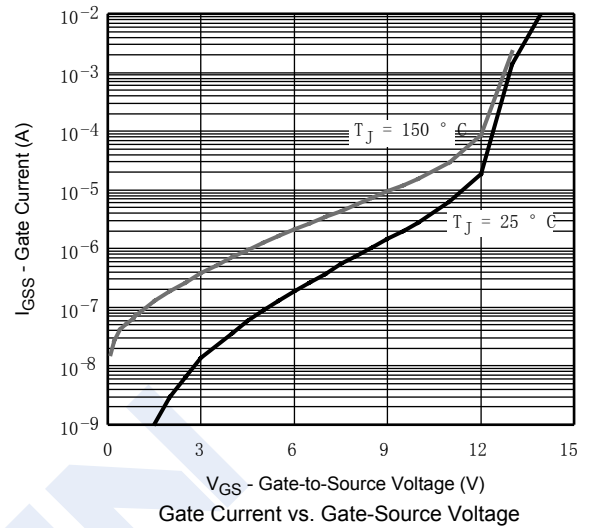
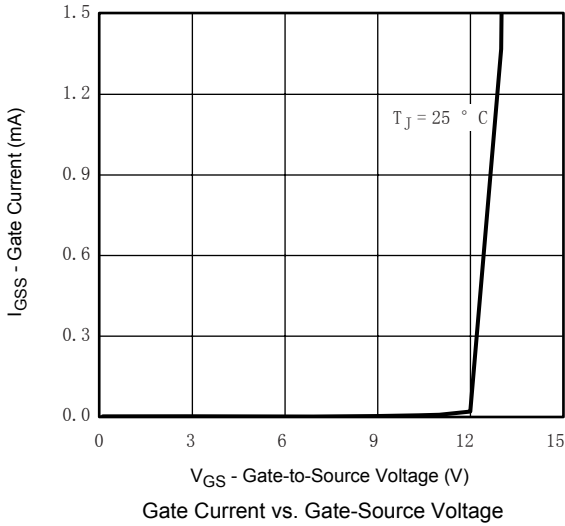
Note. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2 %.

#### ■ Marking

Marking	P6*
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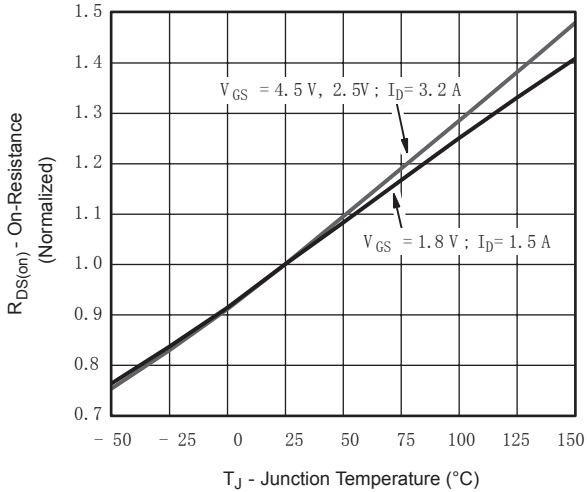
## P-Channel MOSFET SI2377EDS (KI2377EDS)

### Typical Characteristics

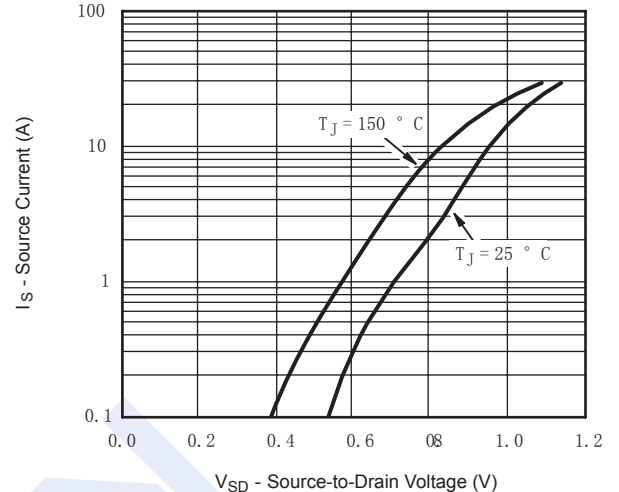


## P-Channel MOSFET SI2377EDS (KI2377EDS)

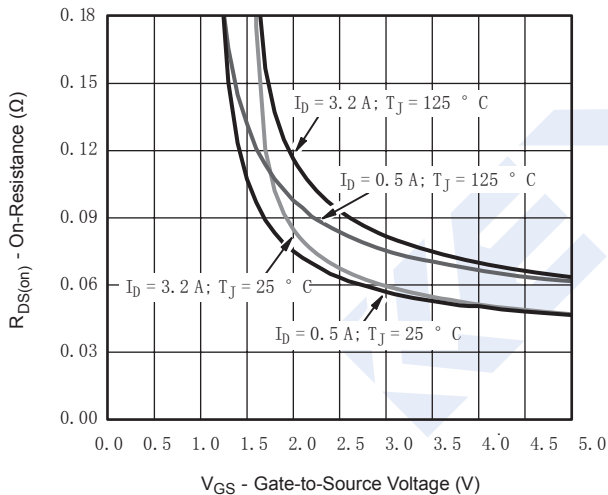
■ Typical Characteristics



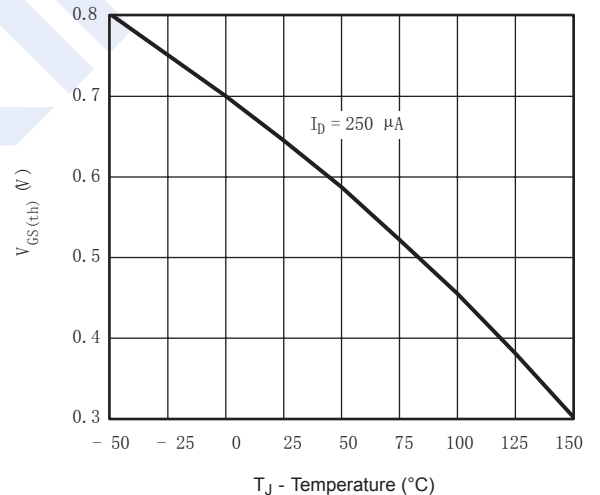
On-Resistance vs. Junction Temperature



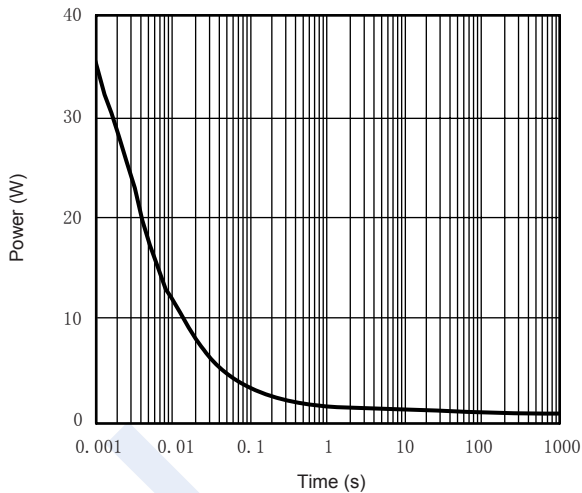
Source-Drain Diode Forward Voltage



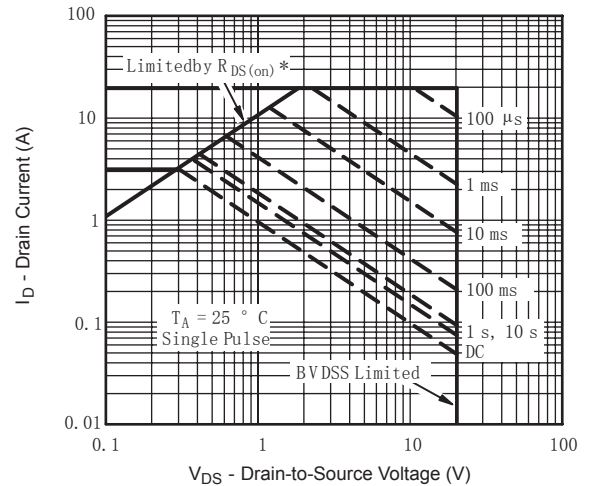
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage



Single Pulse Power, Junction-to-Ambient



\*  $V_{GS} >$  minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified  
Safe Operating Area, Junction-to-Ambient

## P-Channel MOSFET SI2377EDS (KI2377EDS)

■ Typical Characteristics

